

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	4	"5399924".uref.	USPAT	2005/01/10 15:08
2	BRS	L2	892	"326"/\$.ccls. and (memory adj1 cell) and (buffer or driver) and invert\$2 and transistor	USPAT	2005/01/10 16:04
3	BRS	L3	237	2 and (pld or fpga)	USPAT	2005/01/10 17:38
4	BRS	L4	4	"5399924".uref.	USPAT	2005/01/10 17:40
5	IS&R	L5	2	(("5331226") or ("4567385")).PN.	USPAT	2005/01/10 17:41
6	BRS	L6	29	"4567385".uref.	USPAT	2005/01/10 17:46
7	BRS	L7	77	(stand-by or (stand adj1 by)) and (pld or fpga)	USPAT	2005/01/10 17:47
8	BRS	L8	59	7 and memory	USPAT	2005/01/10 17:47
9	BRS	L9	152	(stand-by or (stand adj1 by)) and (pld or fpga)	US- PGPUB; USPAT	2005/01/10 17:47
10	BRS	L10	59	9 and memory	USPAT	2005/01/10 17:48
11	BRS	L11	129	9 and memory	US- PGPUB; USPAT	2005/01/10 18:02
12	IS&R	L12	4	(("5905671") or ("5949706") or ("5896042") or ("5603043")).PN.	USPAT	2005/01/10 18:03
13	BRS	L13	6	"6233169".uref.	USPAT	2005/01/10 18:11
14	BRS	L14	36	11 and transistor	USPAT	2005/01/10 18:11
15	BRS	L15	277	326/37-50.ccls. and (memory adj1 cell) and (tri-state or (high adj1 impedance) or stand-by)	US- PGPUB; USPAT	2005/01/10 18:14